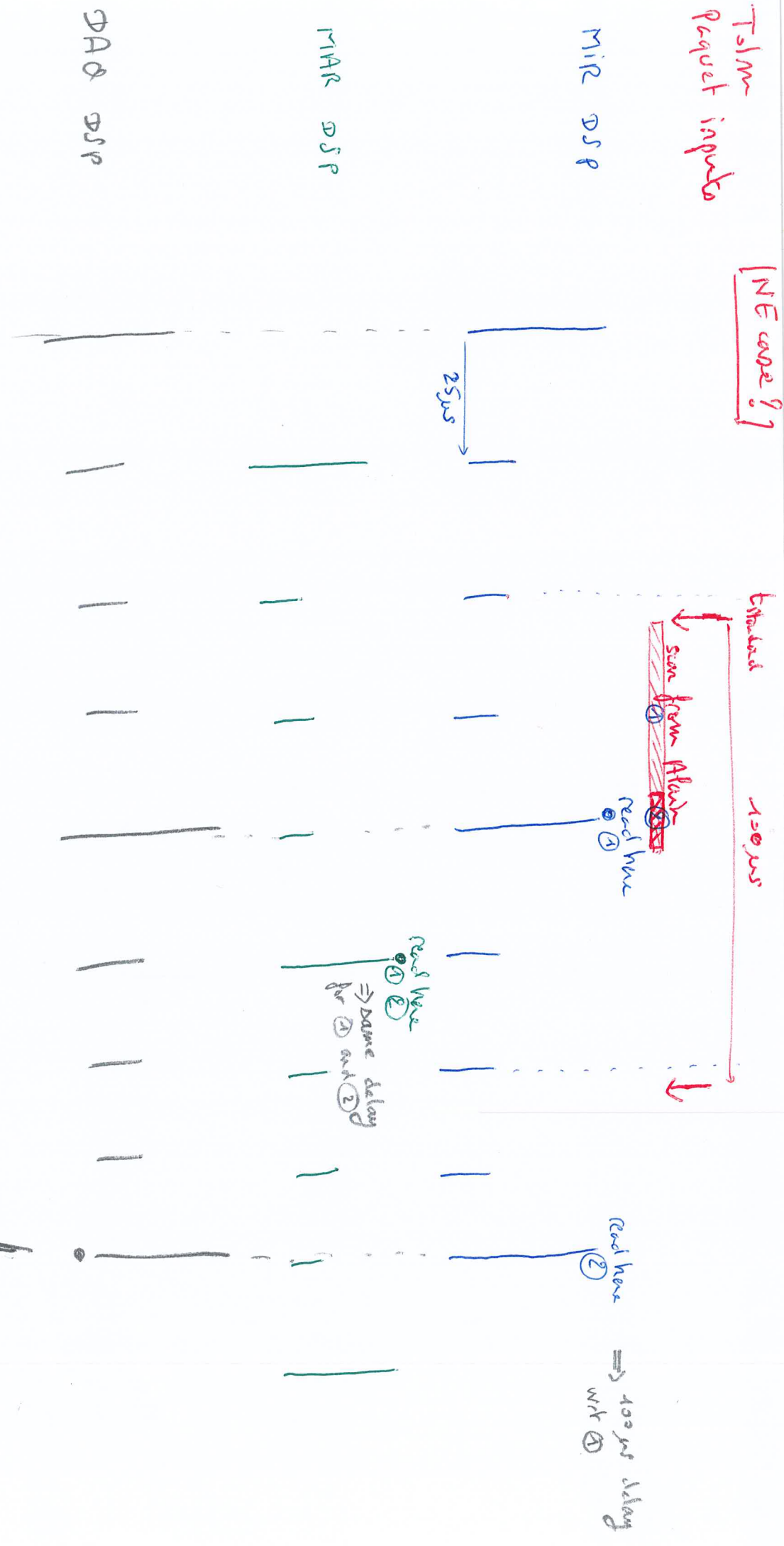


Tslm  
Packet inputs

[NE code?]



→ Packets read every 100µs in the DSP (not every 25µs)

→ CAL to SC delay increased by (70-100µs) at the transition between ① and ② for T1AR

→ CAL to SC delay for T1AR did not change. (may try to scan 25µs more in the communication from RTIC to DSP)